

$V_{DRM}$	=	2500 V
$I_{TGQM}$	=	1500 A
$I_{TSM}$	=	$10 \times 10^3$ A
$V_{T0}$	=	1.45 V
$r_T$	=	0.90 mW
$V_{Dclink}$	=	1400 V

# Asymmetric Gate turn-off Thyristor 5SGA 15F2502

Doc. No. 5SYA1214-02 Oct. 06

- Patented free-floating silicon technology
- Low on-state and switching losses
- Annular gate electrode
- Industry standard housing
- Cosmic radiation withstand rating

## Blocking

Maximum rated values <sup>1)</sup>

Parameter	Symbol	Conditions	min	typ	max	Unit
Repetitive peak off-state voltage	$V_{DRM}$	$V_{GR} \geq 2$ V			2500	V
Repetitive peak reverse voltage	$V_{RRM}$				17	V
Permanent DC voltage for 100 FIT failure rate	$V_{DC-link}$	Ambient cosmic radiation at sea level in open air.			1400	V

Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
Repetitive peak off-state current	$I_{DRM}$	$V_D = V_{DRM}, V_{GR} \geq 2$ V			100	mA
Repetitive peak reverse current	$I_{RRM}$	$V_R = V_{RRM}, R_{GK} = \infty \Omega$			50	mA

## Mechanical data

Maximum rated values <sup>1)</sup>

Parameter	Symbol	Conditions	min	typ	max	Unit
Mounting force	$F_m$		14	15	16	kN

Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
Pole-piece diameter	$D_p$	$\pm 0.1$ mm		47		mm
Housing thickness	H		25.8		26.2	mm
Weight	m				0.6	kg
Surface creepage distance	$D_s$	Anode to Gate	25			mm
Air strike distance	$D_a$	Anode to Gate	15			mm

Note 1 Maximum rated values indicate limits beyond which damage to the device may occur

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## GTO Data

### On-state

Maximum rated values <sup>1)</sup>

Parameter	Symbol	Conditions	min	typ	max	Unit
Max. average on-state current	$I_{T(AV)M}$	Half sine wave, $T_C = 85^\circ\text{C}$			570	A
Max. RMS on-state current	$I_{T(RMS)}$				900	A
Max. peak non-repetitive surge current	$I_{TSM}$	$t_p = 10\text{ ms}$ , $T_{vj} = 125^\circ\text{C}$ , sine wave After Surge: $V_D = V_R = 0\text{ V}$			$10 \times 10^3$	A
Limiting load integral	$I^2t$				$500 \times 10^3$	$\text{A}^2\text{s}$
Max. peak non-repetitive surge current	$I_{TSM}$	$t_p = 1\text{ ms}$ , $T_{vj} = 125^\circ\text{C}$ , sine wave After Surge: $V_D = V_R = 0\text{ V}$			$20 \times 10^3$	A
Limiting load integral	$I^2t$				$200 \times 10^3$	$\text{A}^2\text{s}$

Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
On-state voltage	$V_T$	$I_T = 1500\text{ A}$ , $T_{vj} = 125^\circ\text{C}$			2.8	V
Threshold voltage	$V_{(T0)}$	$T_{vj} = 125^\circ\text{C}$			1.45	V
Slope resistance	$r_T$	$I_T = 300 \dots 2000\text{ A}$			0.90	$\text{m}\Omega$
Holding current	$I_H$	$T_{vj} = 25^\circ\text{C}$			50	A

### Turn-on switching

Maximum rated values <sup>1)</sup>

Parameter	Symbol	Conditions	min	typ	max	Unit
Critical rate of rise of on-state current	$di_T/dt_{cr}$	$T_{vj} = 125^\circ\text{C}$ , $f = 200\text{ Hz}$ $I_T = 1500\text{ A}$ , $I_{GM} = 30\text{ A}$			400	$\text{A}/\mu\text{s}$
Critical rate of rise of on-state current	$di_T/dt_{cr}$	$di_G/dt = 20\text{ A}/\mu\text{s}$ , $f = 1\text{ Hz}$			600	$\text{A}/\mu\text{s}$
Min. on-time	$t_{on}$	$V_D = 0.5 V_{DRM}$ , $T_{vj} = 125^\circ\text{C}$ $I_T = 1500\text{ A}$ , $di/dt = 100\text{ A}/\mu\text{s}$ , $I_{GM} = 30\text{ A}$ , $di_G/dt = 20\text{ A}/\mu\text{s}$ , $C_S = 3\text{ }\mu\text{F}$ , $R_S = 5\text{ }\Omega$	80			$\mu\text{s}$

Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
Turn-on delay time	$t_d$	$V_D = 0.5 V_{DRM}$ , $T_{vj} = 125^\circ\text{C}$ $I_T = 1500\text{ A}$ , $di/dt = 100\text{ A}/\mu\text{s}$ ,			2	$\mu\text{s}$
Rise time	$t_r$	$I_{GM} = 30\text{ A}$ , $di_G/dt = 20\text{ A}/\mu\text{s}$ ,			4	$\mu\text{s}$
Turn-on energy per pulse	$E_{on}$	$C_S = 3\text{ }\mu\text{F}$ , $R_S = 5\text{ }\Omega$			0.5	J

### Turn-off switching

Maximum rated values <sup>1)</sup>

Parameter	Symbol	Conditions	min	typ	max	Unit
Max. controllable turn-off current	$I_{TGQM}$	$V_{DM} \leq V_{DRM}$ , $di_{GQ}/dt = 30\text{ A}/\mu\text{s}$ , $C_S = 3\text{ }\mu\text{F}$ , $L_S \leq 0.3\text{ }\mu\text{H}$			1500	A
Min. off-time	$t_{off}$	$V_D = 0.5 V_{DRM}$ , $T_{vj} = 125^\circ\text{C}$ $V_{DM} \leq V_{DRM}$ , $di_{GQ}/dt = 30\text{ A}/\mu\text{s}$ , $I_{TGQ} = I_{TGQM}$ , $R_S = 5\text{ }\Omega$ , $C_S = 3\text{ }\mu\text{F}$ , $L_S = 0.3\text{ }\mu\text{H}$	80			$\mu\text{s}$

Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
Storage time	$t_s$	$V_D = 0.5 V_{DRM}$ , $T_{vj} = 125^\circ\text{C}$			15	$\mu\text{s}$
Fall time	$t_f$	$V_{DM} \leq V_{DRM}$ , $di_{GQ}/dt = 30\text{ A}/\mu\text{s}$ ,			2	$\mu\text{s}$
Turn-on energy per pulse	$E_{off}$	$I_{TGQ} = I_{TGQM}$ , $R_S = 5\text{ }\Omega$ , $C_S = 3\text{ }\mu\text{F}$ , $L_S = 0.3\text{ }\mu\text{H}$			2	J
Peak turn-off gate current	$I_{GQM}$				480	A

## Gate

Maximum rated values <sup>1)</sup>

Parameter	Symbol	Conditions	min	typ	max	Unit
Repetitive peak reverse voltage	V <sub>GRM</sub>				17	V
Repetitive peak reverse current	I <sub>GRM</sub>	V <sub>GR</sub> = V <sub>GRM</sub>			20	mA

Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
Gate trigger voltage	V <sub>GT</sub>	T <sub>vj</sub> = 25°C,		1.5		V
Gate trigger current	I <sub>GT</sub>	V <sub>D</sub> = 24 V, R <sub>A</sub> = 0.1 Ω		1.5		A

## Thermal

Maximum rated values <sup>1)</sup>

Parameter	Symbol	Conditions	min	typ	max	Unit
Junction operating temperature	T <sub>vj</sub>		0		125	°C
Storage temperature range	T <sub>stg</sub>		0		125	°C

Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
Thermal resistance junction to case	R <sub>th(j-c)</sub>	Double side cooled			27	K/kW
	R <sub>th(j-c)A</sub>	Anode side cooled			49	K/kW
	R <sub>th(j-c)C</sub>	Cathode side cooled			60	K/kW
Thermal resistance case to heatsink (Double side cooled)	R <sub>th(c-h)</sub>	Single side cooled			16	K/kW
	R <sub>th(c-h)</sub>	Double side cooled			8	K/kW

Analytical function for transient thermal impedance:

$$Z_{th(j-c)}(t) = \sum_{i=1}^n R_i(1 - e^{-t/\tau_i})$$

i	1	2	3	4
R <sub>i</sub> (K/kW)	14.570	5.051	7.285	0.097
τ <sub>i</sub> (s)	0.4610	0.0950	0.0120	0.0010

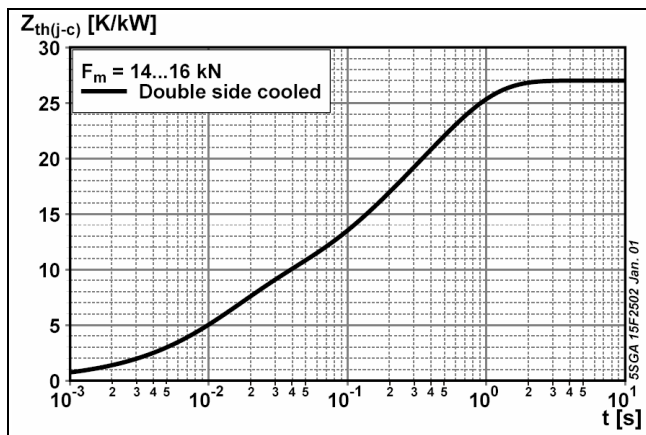


Fig. 1 Transient thermal impedance, junction to case

**Max. on-state characteristic model:**

$$V_{T25} = A_{Tvj} + B_{Tvj} \cdot I_T + C_{Tvj} \cdot \ln(I_T + 1) + D_{Tvj} \cdot \sqrt{I_T}$$

Valid for  $i_T = 300 - 2000$  A

A <sub>25</sub>	B <sub>25</sub>	C <sub>25</sub>	D <sub>25</sub>
$178.9 \times 10^{-3}$	$816.7 \times 10^{-6}$	$356.4 \times 10^{-3}$	$-41.7 \times 10^{-3}$

**Max. on-state characteristic model:**

$$V_{T125} = A_{Tvj} + B_{Tvj} \cdot I_T + C_{Tvj} \cdot \ln(I_T + 1) + D_{Tvj} \cdot \sqrt{I_T}$$

Valid for  $i_T = 300 - 2000$  A

A <sub>125</sub>	B <sub>125</sub>	C <sub>125</sub>	D <sub>125</sub>
$11.7 \times 10^{-3}$	$630.8 \times 10^{-6}$	$340.2 \times 10^{-3}$	$-22.0 \times 10^{-3}$

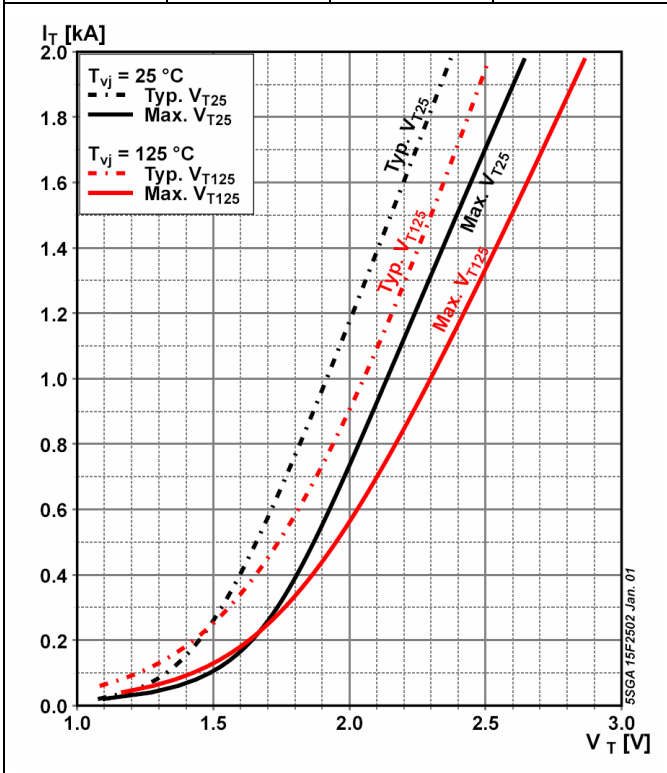


Fig. 2 On-state characteristics

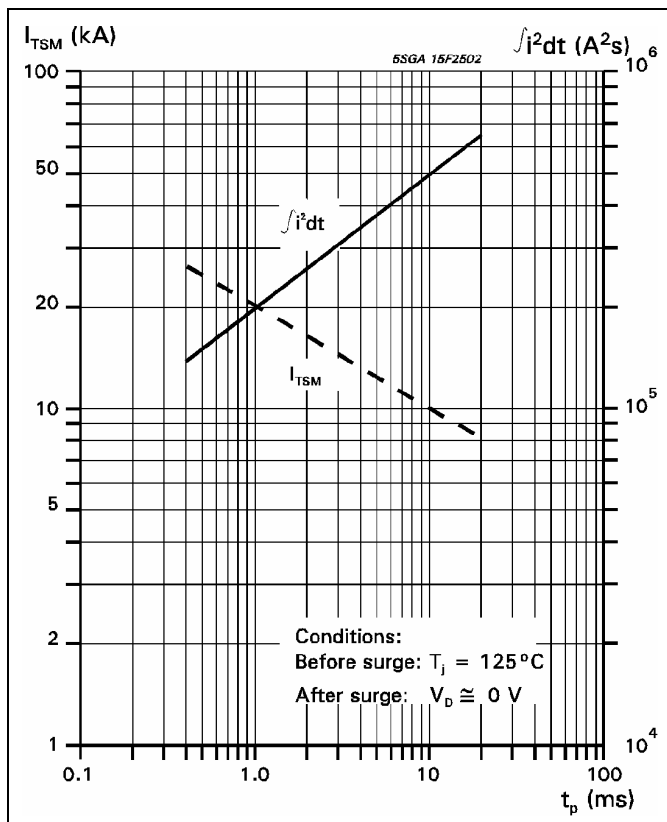


Fig. 3 Surge current and fusing integral vs. pulse width

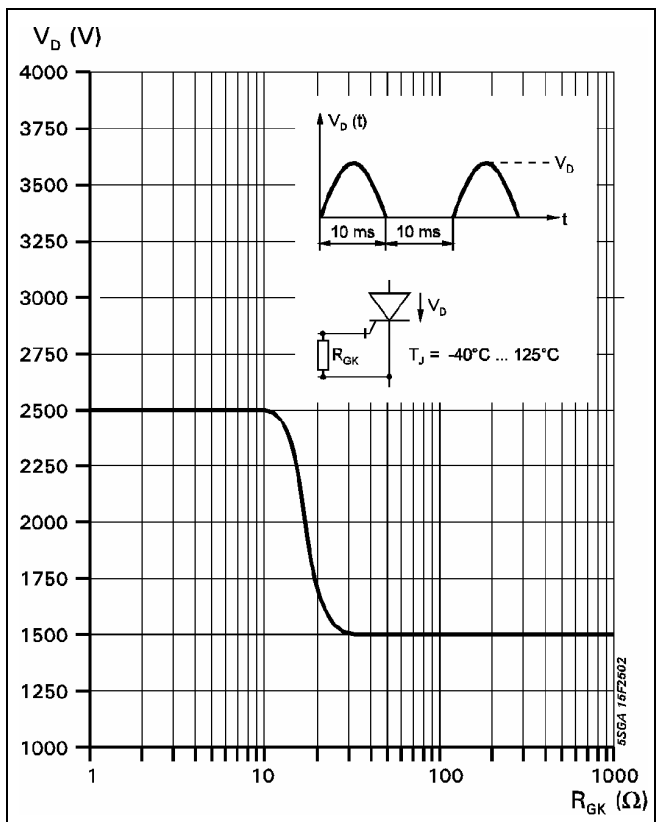


Fig. 4 Forward blocking voltage vs. gate-cathode resistance

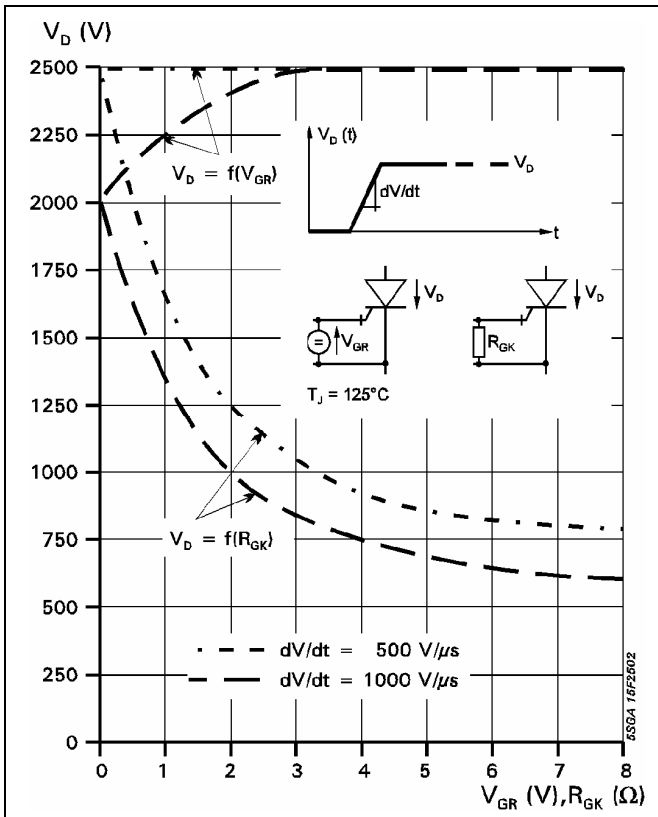


Fig. 5 Static dv/dt capability; forward blocking voltage vs. neg. gate voltage or gate cathode resistance

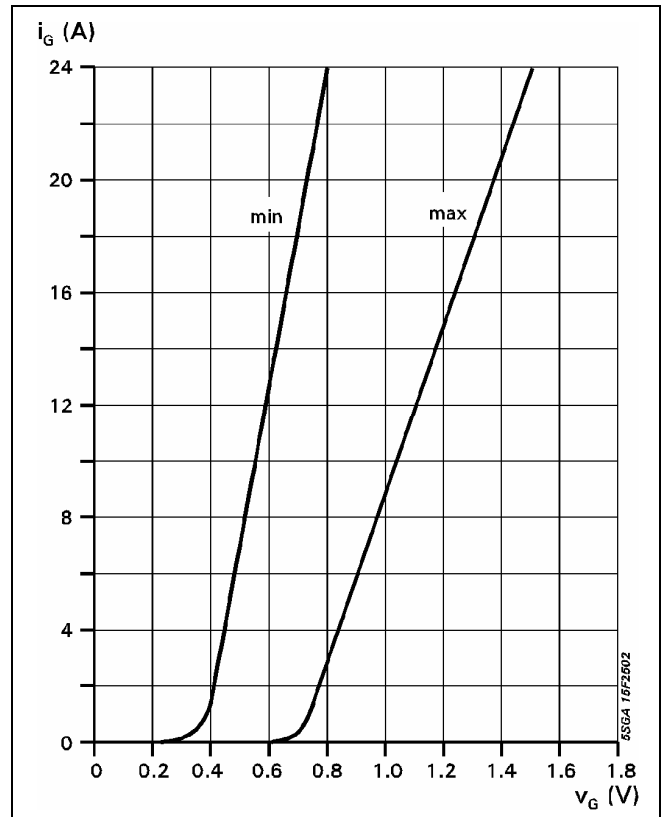


Fig. 6 Forward gate current vs. forward gate voltage

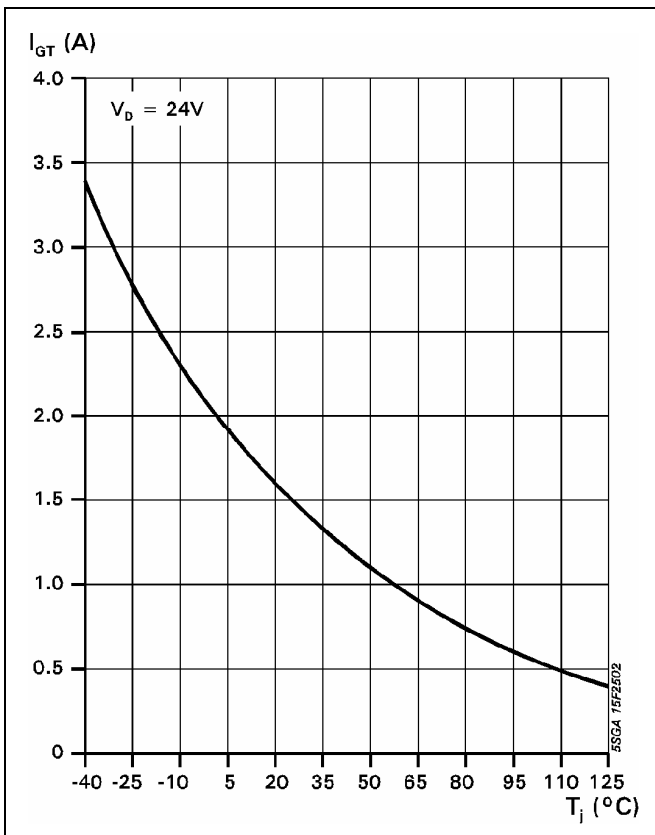
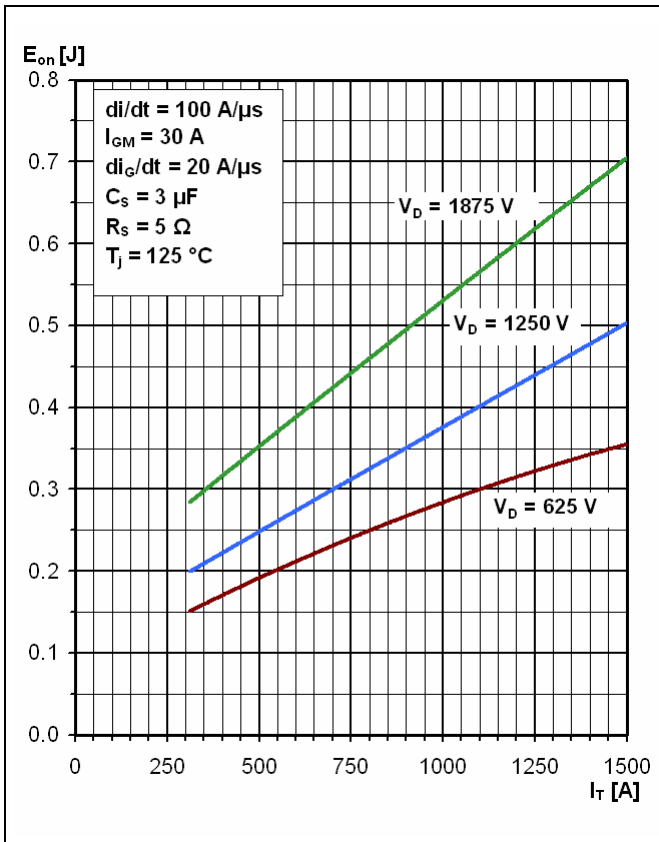
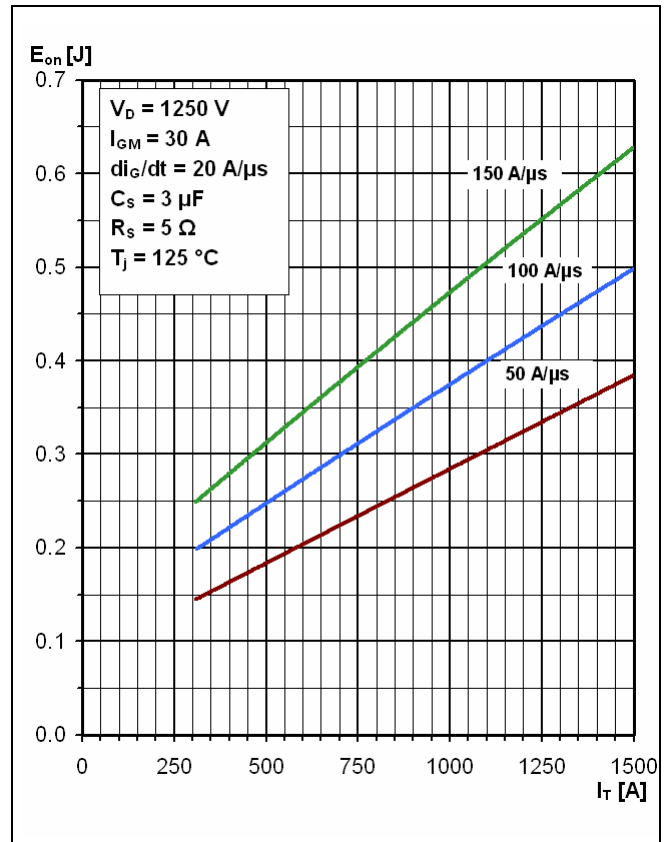


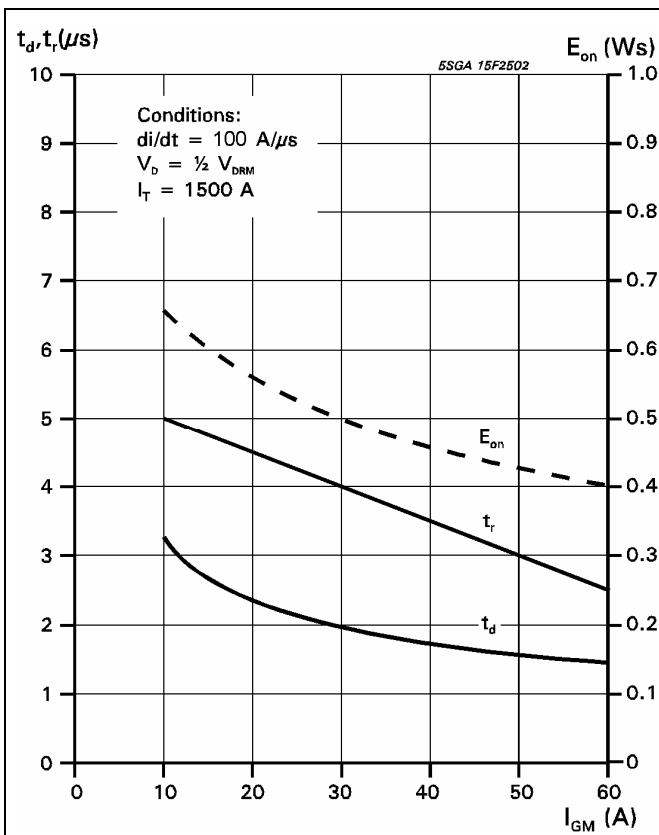
Fig. 7 Gate trigger current vs. junction temperature



**Fig. 8** Turn-on energy per pulse vs. on-state current and turn-on voltage



**Fig. 9** Turn-on energy per pulse vs. on-state current and current rise rate



**Fig. 10** Turn-on energy per pulse vs. on-state current and turn-on voltage

**Common Test conditions:**

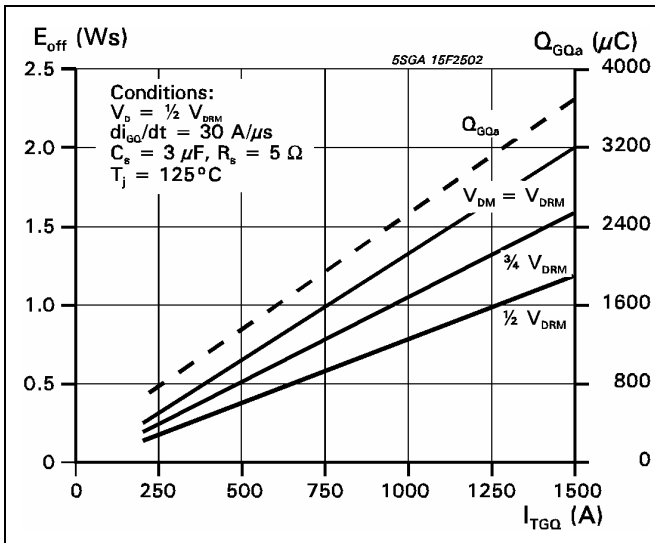
- di<sub>G</sub>/dt = 20 A/μs
- C<sub>S</sub> = 3 μF
- R<sub>S</sub> = 5 Ω
- T<sub>J</sub> = 125 °C

**Definition of Turn-on energy:**

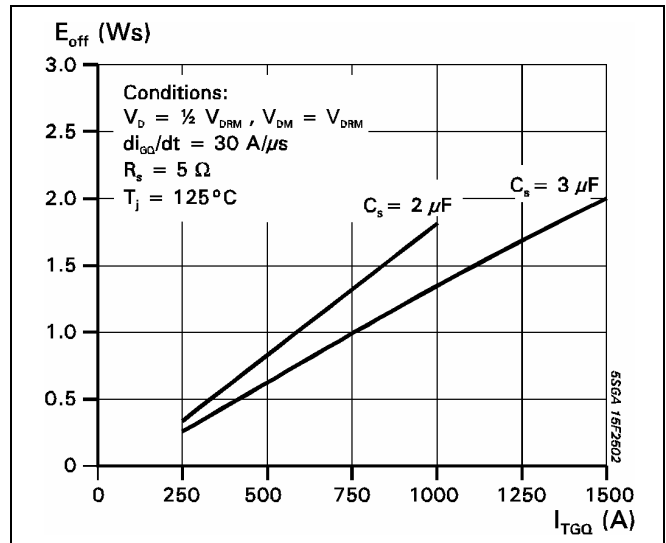
$$E_{on} = \int_0^{20\text{ms}} V_D \cdot I_T dt \quad (t = 0, I_G = 0.1 \cdot I_{GM})$$

**Definition of Turn-off energy:**

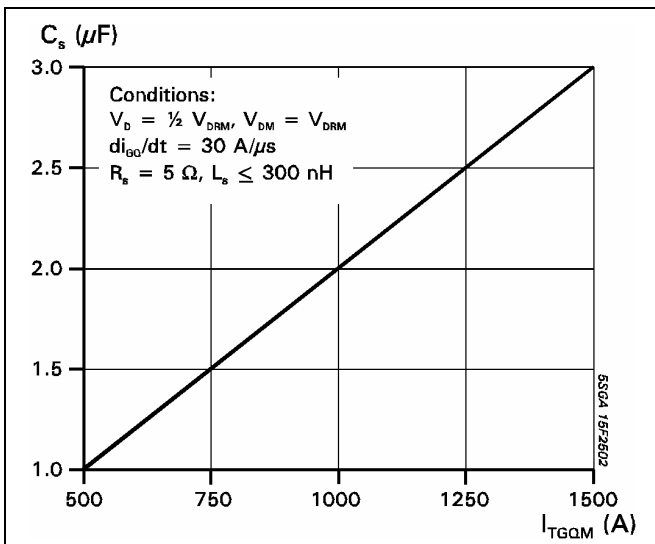
$$E_{off} = \int_0^{40\text{ms}} V_D \cdot I_T dt \quad (t = 0, I_T = 0.9 \cdot I_{TQ})$$



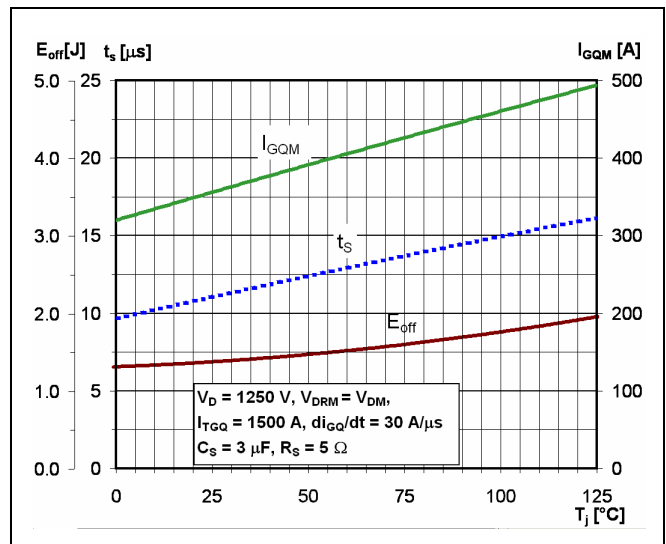
**Fig. 11** Turn-off energy per pulse vs. turn-off current and peak turn-off voltage, extracted gate charge vs. turn-off current



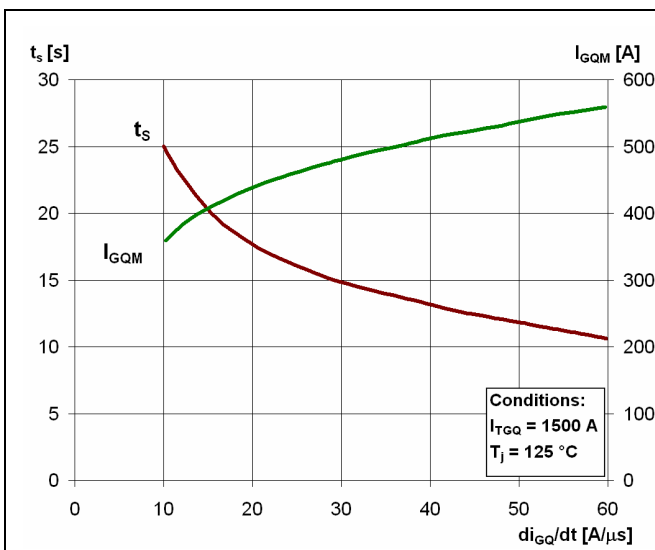
**Fig. 12** Turn-off energy per pulse vs. turn-off current and snubber capacitance



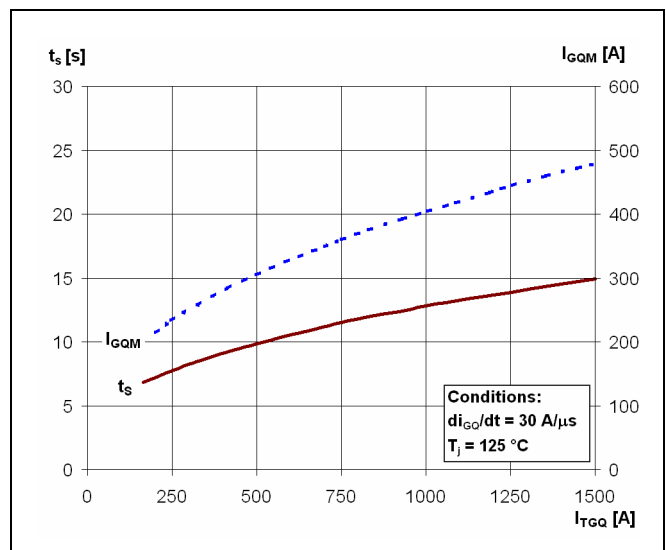
**Fig. 13** Required snubber capacitor vs. max allowable turn-off current



**Fig. 14** Turn-off energy per pulse, storage time and peak turn-off gate current vs. junction temperature



**Fig. 15** Storage time and peak turn-off gate current vs. neg. gate current rise rate



**Fig. 16** Storage time and peak turn-off gate current vs. neg. gate current rise rate

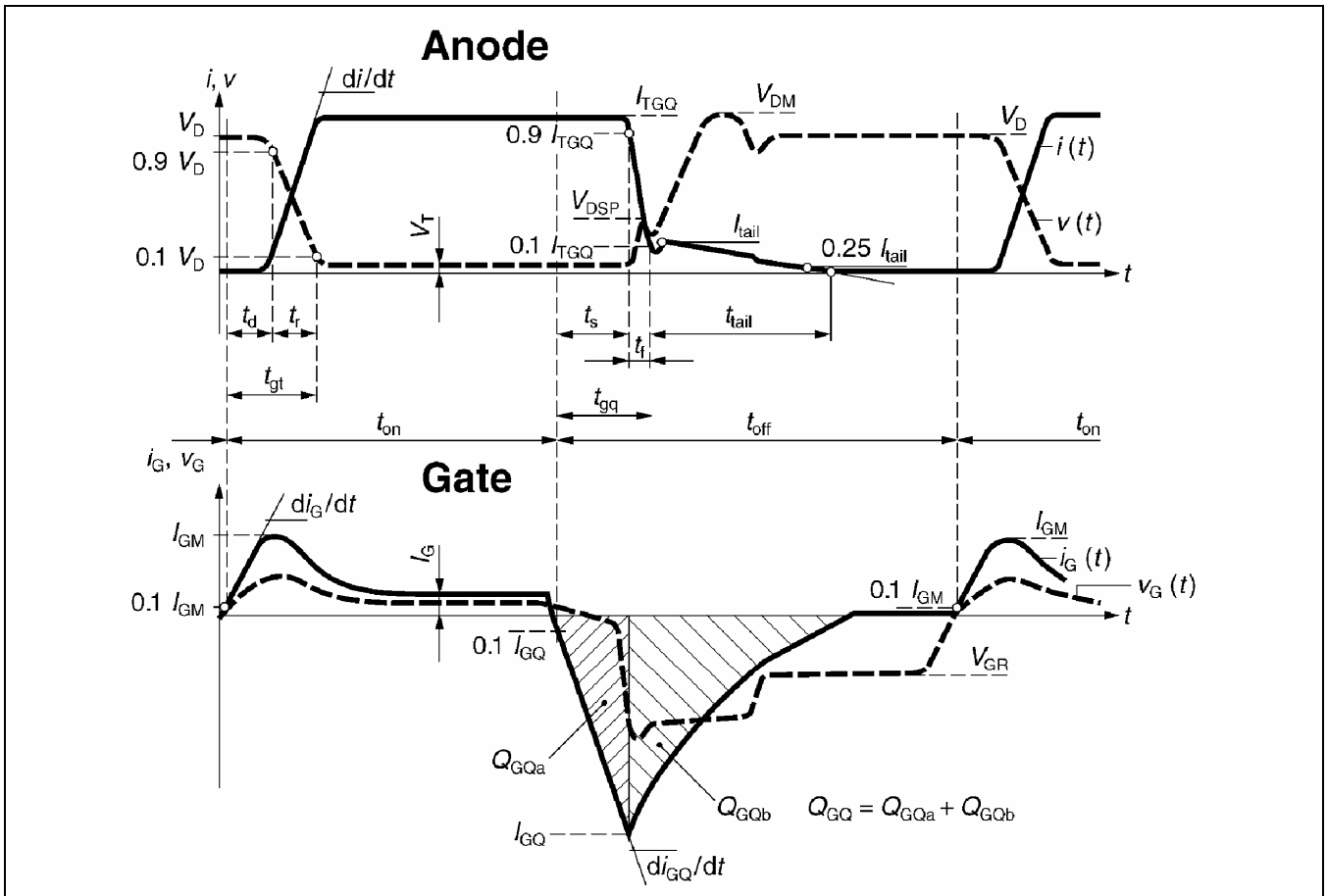


Fig. 17 General current and voltage waveforms with GTO-specific symbols

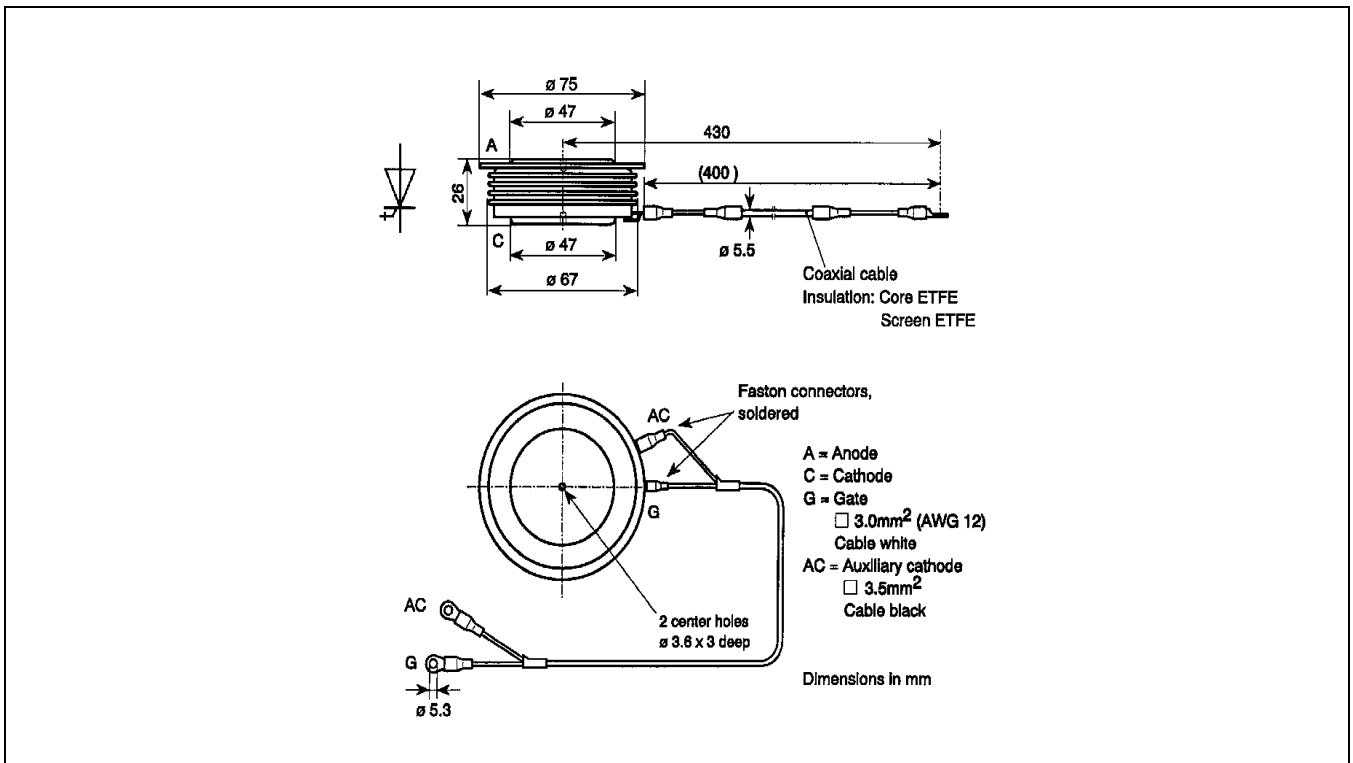


Fig. 18 Outline drawing; all dimensions are in millimeters and represent nominal values unless stated otherwise



## Reverse avalanche capability

In operation with an antiparallel freewheeling diode, the GTO reverse voltage  $V_R$  may exceed the rate value  $V_{RRM}$  due to stray inductance and diode turn-on voltage spike at high  $di/dt$ . The GTO is then driven into reverse avalanche. This condition is not dangerous for the GTO provided avalanche time and current are below 10  $\mu$ s and 1000 A respectively. However, gate voltage must remain negative during this time. Recommendation :  $V_{GR} = 10... 15$  V.

### Related documents:

5SYA 2036	Recommendations regarding mechanical clamping of Press Pack High Power Semiconductors
5SYA 2046	Cosmic Ray
5SZK 9104	Specification of enviromental class for pressure contact GTO, STORAGE available on request, please contact factory
5SZK 9105	Specification of enviromental class for pressure contact GTO, TRANSPORTATION available on request, please contact factory

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**ABB Switzerland Ltd**  
**Semiconductors**  
Fabrikstrasse 3  
CH-5600 Lenzburg, Switzerland

Telephone +41 (0)58 586 1419  
Fax +41 (0)58 586 1306  
Email [abbsem@ch.abb.com](mailto:abbsem@ch.abb.com)  
Internet [www.abb.com/semiconductors](http://www.abb.com/semiconductors)

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